



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN NOVEMBER 2016 Course : Revised Course - 2013

| Seat No: 3101 PR No: 201203659             | Sex: F    |                  | Name: NAIK ASHIKA RAMAKANT |   |                       |                                 |  |
|--|-----------|------------------|----------------------------|---|-----------------------|---------------------------------|--|
| No Of Attempts: 1                          |           | No Of<br>Credits | Gra<br>Obta                |   | SGPA                  | *                               |  |
| Solid State Devices and Semiconductor      | Physics   | 0.000            | 0510                       |   | - 1                   |                                 |  |
|  | Theory    | 4                | AB                         | P                                       |                       |                                 |  |
|  | IA        | 2                | AA                         | P                                       |                       |                                 |  |
| Digital VLSI Design                        |           | -                |                            |   | *                     |                                 |  |
|  | Theory    | 4                | CC                         | P                                       |                       |                                 |  |
|  | IA        | 2                | AA                         | P                                       |                       |                                 |  |
| Analog VLSI Design                         |           |                  |                            |   |                       | (.90)                           |  |
|  | Theory    | 4                | BC                         | P                                       |                       |                                 |  |
|  | IA        | 2                | AB                         | P                                       |                       |                                 |  |
| VLSI Technology                            |           |                  |                            |   |                       |                                 |  |
|  | Theory    | 4                | BB                         | Р                                       | W.                    |                                 |  |
| ×  | IA        | 2                | AB                         | P                                       |                       |                                 |  |
| Advanced Engineering Mathematics           |           |                  |                            |   |                       |                                 |  |
|  | Theory    | 4                | BB                         | Р                                       |                       |                                 |  |
|  | IA        | 2                | AB                         | P                                       |                       |                                 |  |
| HDL - LAB                                  |           |                  |                            |   | 20                    |                                 |  |
|  | IA        | 2                | AB                         | P                                       |                       |                                 |  |
|  | Practical | 2                | BC                         | P                                       |                       |                                 |  |
| VLSI CAD - LAB                             |           |                  |                            |   |                       |                                 |  |
|  | IA        | 2                | AA                         | P                                       |                       |                                 |  |
|  | Practical | 2                | AB                         | P                                       |                       |                                 |  |
|  | Total:    | 38               | <del></del>                | *************************************** | 7.32 P<br>PASSES      | PAPCHERIAS PATERBANAS AND AND A |  |
| Seat No : 3102 P R No : 201107721          | Se        | x: F             | Nam                        | e: P                                    | HADTE PRIYANKA DHARMA | WWW.FIRENOVER                   |  |
| No Of Attempts: 1                          |           | No Of            | Grad                       |   |                       |                                 |  |
| 3297                                       |           | Credits          | Obtai                      |   | SGPA                  |                                 |  |
| Solid State Devices and Semiconductor      | Physics   |                  | 55.01                      |   |                       |                                 |  |
|  | Theory    | 4                | FF                         | F                                       |                       |                                 |  |
|  | IA        | 2                | CC                         | Р                                       |                       |                                 |  |
| Digital VLSI Design                        |           |                  |                            |   |                       |                                 |  |
| Seeder (COL)   Hardenburger (Seeder)   COL | Theory    | 4                | AB                         | Р                                       |                       |                                 |  |
|  | IA        | 2                | вв                         | Р                                       |                       |                                 |  |
| Analog VLSI Design                         |           |                  |                            |   |                       |                                 |  |
| (F)(W) (F)                                 | Theory    | 4                | FF                         | F                                       |                       |                                 |  |
|  | IA        | 2                | BC                         | P                                       |                       |                                 |  |
| VLSI Technology                            |           |                  |                            |   |                       |                                 |  |
|  |           |                  |                            |   |                       |                                 |  |



Theory

Theory IA

Practical

Practical

Total:

IA

IA

IA

Advanced Engineering Mathematics

HDL - LAB

VLSI CAD - LAB

FF F

BC P

CC P

CC P

AB P

BB P

AB P

BB P

4.47 F FAILS

2

2

2

2

2

2

38



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION HELD IN NOVEMBER 2016

Course: Revised Course - 2013 COLLEGE: GOA COLLEGE OF ENGINEERING Seat No: 3103 Sex: F Name: RASHINKAR PRAGATI GANAPATHI No Of Attempts: 1 No Of Grade SGPA Credits Obtained Solid State Devices and Semiconductor Physics CC IA 2 BC P Digital VLSI Design Theory CC P IA 2 BC P Analog VLSI Design Theory FF IA 2 CC P VLSI Technology Theory CC Р IA 2 BB P Advanced Engineering Mathematics Theory CC IA 2 CC HDL - LAB 2 AB P Practical 2 ВВ Р VLSI CAD - LAB IA 2 AB P Practical 2 BB P Total: 38 5.21 F FAILS Seat No: 3104 PRNo: 201107728 Sex: F Name: RODRIGUES FROILA VALENCY No Of Attempts: 1 No Of Grade **SGPA** Credits Obtained Solid State Devices and Semiconductor Physics Theory 4 AB P IA 2 AB Р Digital VLSI Design Theory AR Р IA 2 AA P Analog VLSI Design Theory 4 AA P IA 2 AO P VLSI Technology Theory 4 BC P IA 2 AA Р Advanced Engineering Mathematics Theory 4 AB Р IA 2 BB P HDL - LAB 2 IA AO P Practical 2 AA P VLSI CAD - LAB

2

2

38

AA Ρ

AA Ρ

8.32 P



Practical

Total:

P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cummulative Grade Point Average



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION Course : Revised Course - 2013

| COLLEGE: GOA COLLEGE OF F | ENCINEEDING |
|---------------------------|-------------|
|---------------------------|-------------|

| Seat No: 3105 PR No: 200800873                       |               | 200800872    | Sex: M    |                  | Name: NORONHA ALWIN        |              | NORONHA ALWIN     |  |
|--|---------------|--------------|-----------|------------------|----------------------------|--------------|-------------------|--|
| Solid State De                                       | evices and Se | emiconductor | Physics   | No Of<br>Credits | Gra<br>Obta                | ade<br>ained | SGPA              |  |
| ( to )   |               |              | Theory    | 4                | CC                         | +            |                   |  |
|  |               |              | IA        | 2                | BB                         | +            |                   |  |
| Digital VLSI D                                       | esign         |              |           | . *              | UU                         |              | 87                |  |
|  |               |              | Theory    | 4                | ВС                         | +            |                   |  |
| Analog VLSI  | esign         |              | IA        | 2                | BB                         | +            |                   |  |
|  |               |              | Theory    | 4                | CC                         | +            |                   |  |
| VLSI Technolo  | gy            |              | IA        | 2                | BB                         | +            |                   |  |
|  |               | Theory       | 4         | CC               | . 4                        |              |                   |  |
| Advanced Eng   | ineering Mat  | nematics     | IA        | 2                | AB                         | +            |                   |  |
|  | 85.           | 5.50         | Theory    | 4                | CC                         | Р            |                   |  |
| HDL - LAB  |               |              | IA        | 2                | CC                         | +            |                   |  |
|  |               |              | IA        | 2                | ВВ                         | +            |                   |  |
| VLSI CAD - LAI                                       | 3             |              | Practical | 2                | BC                         | +            |                   |  |
|  | (4)           |              | IA        | 2                | AB                         | +            |                   |  |
|  |               |              | Practical | 2                |                            | +            |                   |  |
|  |               |              | Total:    | 38               | *************              |              | 6.05 P<br>PASSES  |  |
| eat No : 3106 PR No : 201107719<br>o Of Attempts : 2 |               | 01107719     | Sex: M    |                  | Name: PATIL ABHIJEET VILAS |              |                   |  |
|  |               |              |           | No Of            | 01                         |              | WILL VIDE I VILAS |  |

|  |            |                  |   |   | PASSES               |                                       |
|--|------------|------------------|---|---|----------------------|---------------------------------------|
| Seat No : 3106 P R No : 20110771<br>No Of Attempts : 2 | 9 S        | Sex: M           |   | ne :                                    | PATIL ABHIJEET VILAS | · · · · · · · · · · · · · · · · · · · |
| Solid State Devices and Semiconduct                    | or Physics | No Of<br>Credits | Gra                                     |   | SGPA                 |                                       |
|  | Theory     | 4                | ВС                                      | Р                                       |                      |                                       |
| Digital VLSI Design                                    | IA         | 2                | ВС                                      | +                                       |                      |                                       |
|  | Theory     | 4                | CC                                      | Р                                       |                      |                                       |
| Analog VLSI Design                                     | IA         | 2                | CC                                      | +                                       |                      |                                       |
|  | Theory     | 4                | CC                                      | Р                                       |                      | 161                                   |
| VLSI Technology  | IA         | 2                | BC                                      | +                                       |                      |                                       |
|  | Theory     | 4                | CC                                      | +                                       |                      |                                       |
| Advanced Engineering Mathematics                       | IA         | 2                | CC                                      | +                                       |                      |                                       |
|  | Theory     | 4                | CC                                      | +                                       |                      |                                       |
| HDL - LAB  | IA         | 2                |   | +                                       |                      |                                       |
|  | IA         | 2                | AB                                      | +                                       |                      |                                       |
| VLSI CAD - LAB   | Practical  | 2                | ВВ                                      | +                                       |                      | 27                                    |
|  | IA         | 2                | ВВ                                      | +                                       |                      |                                       |
|  | Practical  | 2                | CC                                      | +                                       |                      |                                       |
|  | Total:     | 38               | *************************************** | *************************************** | 5.58 P               | HOZZ                                  |

Grade Grade Performance Points AO 10 Outstanding AA 9 Excellent AB 8 Very Good ВВ Good BC 6 Fair CC 5 Satisfactory FF 0

13/2/2017 S.S.J. Figueiredo

Leo V. Macedo Controller Of Examinations

**PASSES** 

Prof. Y. V. Reddy

Assistant Registrar-E(Proff.) P: Passes; F: Fails; A/ABS: Absent; N/NAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average;