



COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No: 3101 P R No: 201203659 Sex: F Name: NAIK ASHIKA RAMAKANT
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	AB P	
	IA	2	AA P	
Digital VLSI Design	Theory	4	CC P	
	IA	2	AA P	
Analog VLSI Design	Theory	4	BC P	
	IA	2	AB P	
VLSI Technology	Theory	4	BB P	
	IA	2	AB P	
Advanced Engineering Mathematics	Theory	4	BB P	
	IA	2	AB P	
HDL - LAB	IA	2	AB P	
	Practical	2	BC P	
VLSI CAD - LAB	IA	2	AA P	
	Practical	2	AB P	
Total :		38		7.32 P PASSES

Seat No: 3102 P R No: 201107721 Sex: F Name: PHADTE PRIYANKA DHARMA
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	FF F	
	IA	2	CC P	
Digital VLSI Design	Theory	4	AB P	
	IA	2	BB P	
Analog VLSI Design	Theory	4	FF F	
	IA	2	BC P	
VLSI Technology	Theory	4	FF F	
	IA	2	BC P	
Advanced Engineering Mathematics	Theory	4	CC P	
	IA	2	CC P	
HDL - LAB	IA	2	AB P	
	Practical	2	BB P	
VLSI CAD - LAB	IA	2	AB P	
	Practical	2	BB P	
Total :		38		4.47 F FAILS

2480
15/2/17

861
15/2/17

Krl



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2016
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No: 3103 P R No: 201611666 Sex: F Name: RASHINKAR PRAGATI GANAPATHI
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	CC P	
	IA	2	BC P	
Digital VLSI Design	Theory	4	CC P	
	IA	2	BC P	
Analog VLSI Design	Theory	4	FF F	
	IA	2	CC P	
VLSI Technology	Theory	4	CC P	
	IA	2	BB P	
Advanced Engineering Mathematics	Theory	4	CC P	
	IA	2	CC P	
HDL - LAB	IA	2	AB P	
	Practical	2	BB P	
VLSI CAD - LAB	IA	2	AB P	
	Practical	2	BB P	
Total :		38		5.21 F FAILS

Seat No: 3104 P R No: 201107728 Sex: F Name: RODRIGUES FROILA VALENCY
No Of Attempts : 1

		No Of Credits	Grade Obtained	SGPA
Solid State Devices and Semiconductor Physics	Theory	4	AB P	
	IA	2	AB P	
Digital VLSI Design	Theory	4	AB P	
	IA	2	AA P	
Analog VLSI Design	Theory	4	AA P	
	IA	2	AO P	
VLSI Technology	Theory	4	BC P	
	IA	2	AA P	
Advanced Engineering Mathematics	Theory	4	AB P	
	IA	2	BB P	
HDL - LAB	IA	2	AO P	
	Practical	2	AA P	
VLSI CAD - LAB	IA	2	AA P	
	Practical	2	AA P	
Total :		38		8.32 P PASSES

Kul



RESULT REGISTER FOR M.E ELECTRONICS & TELECOMMUNICATION (MICROELECTRONICS) SEMESTER - I EXAMINATION
HELD IN NOVEMBER 2016
Course : Revised Course - 2013

COLLEGE : GOA COLLEGE OF ENGINEERING

Seat No : 3105	P R No : 200800872	Sex : M	Name : NORONHA ALWIN
No Of Attempts : 4			
		No Of Credits	Grade Obtained SGPA
Solid State Devices and Semiconductor Physics	Theory	4	CC +
	IA	2	BB +
Digital VLSI Design	Theory	4	BC +
	IA	2	BB +
Analog VLSI Design	Theory	4	CC +
	IA	2	BB +
VLSI Technology	Theory	4	CC +
	IA	2	BB +
Advanced Engineering Mathematics	Theory	4	CC P
	IA	2	AB +
HDL - LAB	IA	2	BB +
	Practical	2	BC +
VLSI CAD - LAB	IA	2	AB +
	Practical	2	AB +
Total :		38	6.05 P PASSES

Seat No : 3106	P R No : 201107719	Sex : M	Name : PATIL ABHIJEET VILAS
No Of Attempts : 2			
		No Of Credits	Grade Obtained SGPA
Solid State Devices and Semiconductor Physics	Theory	4	BC P
	IA	2	BC +
Digital VLSI Design	Theory	4	CC P
	IA	2	CC +
Analog VLSI Design	Theory	4	CC P
	IA	2	BC +
VLSI Technology	Theory	4	CC +
	IA	2	CC +
Advanced Engineering Mathematics	Theory	4	CC +
	IA	2	CC +
HDL - LAB	IA	2	AB +
	Practical	2	BB +
VLSI CAD - LAB	IA	2	BB +
	Practical	2	CC +
Total :		38	5.58 P PASSES

Grade	Grade Points	Performance
AO	10	Outstanding
AA	9	Excellent
AB	8	Very Good
BB	7	Good
BC	6	Fair
CC	5	Satisfactory
FF	0	Fail

Read By : *P. Pinto*

Checked By : *K.R.*

Date : 14/2/2017

S.S.J. Figueiredo
13/2/2017
S.S.J. Figueiredo
Assistant Registrar-E(Prof.)

Leo V. Macedo
13/2/17
Leo V. Macedo
Controller Of Examinations



Y.V. Reddy
14/2
Prof. Y. V. Reddy
Registrar

P: Passes; F: Fails; A/ABS: Absent; NNAP: Non Appearance; X/NE: Not Eligible; +: Grades Carried Over; SGPA: Semester Grade Point Average; CGPA: Cumulative Grade Point Average

Discrepancy, if any in the Result shall be informed to the University within a week from the date of receipt of the result by the college